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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/992,725	11/14/2001	Ling Cen	42390P12345	5896

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EXAMINER

VU, TRISHA U

ART UNIT PAPER NUMBER

2112

DATE MAILED: 08/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/992,725	Applicant(s) CEN, LING	
	Examiner Trisha Vu	Art Unit 2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE ____ MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 May 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 and 16-30 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 6 is/are allowed.
- 6) ☐ Claim(s) ____ is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 November 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____ |

DETAILED ACTION

1. Claims 1-14 and 16-30 are presented for examination.
2. Claim 15 was canceled by Applicant in the Amendment filed 05/30/2006.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-14 and 16-30 are rejected under 35 U.S.C. 102(b) as being anticipated by Enstrom et al. (U.S. Patent 5,768,542, hereinafter Enstrom).

As for claims 1, 10, 17, 20, 26 and 28, Enstrom teaches selecting a first portion of local node elements from a plurality of local node elements (logical devices) in a first node (a first card), wherein the plurality of local node elements are in an active state and are not enumerated; de-activating a remaining portion of local node elements (inactive logical devices); and enumerating the plurality of local node elements in the first node with the selected first portion of local node elements (the active logical devices) (note col. 4 line 48 et seq. wherein a portion of logical devices in a first card is selected for enumeration) substantially in parallel with local enumeration of local node elements in a second node (note col. 4 line 48 et seq. and especially claims 10 and 18, wherein the configuring of a card occurs in parallel with configuring of any other cards in the system).

As for claims 2, 11, 12, 14, 18, 21, 22, 25, 27 and 30, Enstrom further teaches wherein selecting the first portion includes selecting the portion (e.g. selecting a logical device upon which the configuration will operate, col. 10 lines 19-22) which first accesses a

device (e.g. resources such as memory, I/O..., col. 5 lines 1- to 64, card control register 355, logical device register 350, logical device configuration register 340, logical device number register, etc., col. 5 line 57 to col. 6 line 67 and col. 9 line 46 to col. 10 line 46) that is shared by the plurality of local node elements.

As for claims 3 and 22, Enstrom further teaches wherein selecting the first portion includes selecting the first portion of local node processor elements (e.g. selecting a logical device upon which the configuration will operate, col. 10 lines 19-22).

As for claim 4, Enstrom further teaches wherein de-activating the remaining portion includes putting the remaining portion into a hibernation state (put the non-boot logical devices into inactive state, col. 9 lines 37-44).

As for claims 5, 14 and 24, Enstrom further teaches disabling a link interface between a local node and a larger system upon power up, wherein the larger system includes multiple nodes (plurality of circuit cards) and the link interface allows information to be communicated between the local node and components of the larger system (col. 4 line 48 et seq. wherein link communication with other components of the system is disabled upon power up because the card first needs to be configured to resolve any resources conflict with others in the system).

As for claims 7, 13, 13 and 24, Enstrom further teaches monitoring the enumeration progress of the plurality of local node elements; selecting a second portion of local node elements from the plurality of local node elements if there is an enumeration progress issue; enumerating the plurality of local node elements with the second portion of local node elements if there is an enumeration progress issue (e.g. a logical device is selected for enumeration in response to its logical device register and activate register being set, col. 9 line 37 to col. 10 line 46, and col. 6 lines 8-61).

As for claims 8 and 16, Enstrom further teaches selecting the portion which first accesses

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a device that is shared includes selecting the portion which first reads from a shared register (e.g. resources such as memory, I/O..., col. 5 lines 1- to 64, card control register 355, logical device register 350, logical device configuration register 340, logical device number register, etc., col. 5 line 57 to col. 6 line 67 and col. 9 line 46 to col. 10 line 46).

As for claims 9, 19 and 29, Enstrom further teaches enabling the link interface after enumerating the local node (col. 4 line 48 et seq. wherein after enumeration, when resources conflict is resolved, link communication with others is enabled).

Allowable Subject Matter

4. Claim 6 is allowed as indicated in the previous Office Action.

Response to Arguments

5. Applicant's arguments filed 05/30/2006 with respect to newly amended subject matter "substantially in parallel with local enumeration of local node elements in a second node" have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period


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will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trisha Vu whose telephone number is 571-272-3643. The examiner can normally be reached on Mon-Thur and alternate Fri 8:00am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


Trisha Vu
Examiner
Art Unit 2112

uv


REHANA PERVEEN
SUPERVISORY PATENT EXAMINER
8/2/06